What is Claimed is:

1. A method for routing conductors in an integrated circuit design comprising the steps of:

determining the number of sensitive conductors requiring placement into quiet track locations, wherein a quiet track location is defined as any track location immediately adjacent to a stable conductor;

determining the number of quiet track locations available in said integrated circuit design;

routing one or more sensitive conductors into one or more quiet track locations.

2. The method of claim 1 further comprising the step of:

ranking one or more sensitive conductors according to the relative desirability of said one or more sensitive conductors being placed into a quiet environment, as compared to other conductors; and

wherein said routing step further includes the step of routing said ranked sensitive conductors, according to said ranking.

3. The method of claim 2 further comprising the step of:

ranking one or more preferred track locations according to whether said one or more preferred track locations are adjacent to one or more stable conductors; and

wherein said routing step further includes the step of routing said ranked sensitive conductors, according to said track location ranking, and said sensitive conductor ranking.

4. A computer system for routing conductors in an integrated circuit design, the computer system comprising:

a processor; and

a memory having stored therein the following

means for determining the number of sensitive conductors requiring placement into a quiet track location, wherein a quiet track location is defined as any track location immediately adjacent to a stable conductor;

means for determining the number of quiet track locations available in said integrated circuit design;

means for routing one or more sensitive conductors into one or more quiet track locations.

5. The computer system according to claim 4, the memory further having stored therein the following:

means for ranking one or more sensitive conductors according to the relative desirability of said one or more sensitive conductors being placed into a quiet environment, as compared to other conductors; and

means for routing said ranked sensitive conductors, according to said ranking.

6. The computer system according to claim 4, the memory further having stored therein the following:

means for ranking one or more preferred track locations according to whether said one or more preferred track locations are adjacent to one or more stable conductors; and

means for routing said ranked sensitive conductors, according to said track location ranking, and said sensitive conductor ranking.

7. A machine-readable medium disposed on a computer to perform a method for routing conductors in an integrated circuit design, the method comprising the steps of:

determining the number of sensitive conductors requiring placement into a quiet track location, wherein a quiet track location is defined as any track location immediately adjacent to a stable conductor;

determining the number of quiet track locations available in said integrated circuit design;

routing one or more sensitive conductors into one or more quiet track locations.

8. The machine-readable medium of claim 7, the method therein further comprising the step of:

ranking one or more sensitive conductors according to the relative desirability of said one or more sensitive conductors being placed into a quiet environment, as compared to other conductors; and

wherein said routing step further includes the step of routing said ranked sensitive conductors, according to said ranking.

9. The machine-readable medium of claim 8, the method therein further comprising the step of:

ranking one or more preferred track locations according to whether said one or more preferred track locations are adjacent to one or more stable conductors; and

wherein said routing step further includes the step of routing said ranked sensitive conductors, according to said track location ranking and said sensitive conductor ranking.